

AO4468
N-Channel Enhancement Mode Field Effect Transistor

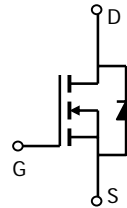
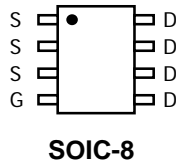
General Description

The AO4468 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications. The source leads are separated to allow a Kelvin connection to the source, which may be used to bypass the source inductance.

Standard Product AO4468 is Pb-free (meets ROHS & Sony 259 specifications). AO4468L is a Green Product ordering option. AO4468 and AO4468L are electrically identical.

Features

$V_{DS} (V) = 30V$
 $I_D = 11.6A \quad (V_{GS} = 10V)$
 $R_{DS(ON)} < 14m\Omega \quad (V_{GS} = 10V)$
 $R_{DS(ON)} < 22m\Omega \quad (V_{GS} = 4.5V)$


Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

| Parameter | Symbol | Maximum | Units |
|--|----------------|------------------|------------|
| Drain-Source Voltage | V_{DS} | 30 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Continuous Drain Current ^A | I_D | $T_A=25^\circ C$ | 11.6 |
| | | $T_A=70^\circ C$ | 9.2 |
| Pulsed Drain Current ^B | I_{DM} | 50 | A |
| Power Dissipation | P_D | $T_A=25^\circ C$ | 3.1 |
| | | $T_A=70^\circ C$ | 2 |
| Junction and Storage Temperature Range | T_J, T_{STG} | -55 to 150 | $^\circ C$ |

Thermal Characteristics

| Parameter | Symbol | Typ | Max | Units |
|--|-----------------|--------------|-----|--------------|
| Maximum Junction-to-Ambient ^A | $R_{\theta JA}$ | 31 | 40 | $^\circ C/W$ |
| Maximum Junction-to-Ambient ^A | | Steady-State | 59 | 75 |
| Maximum Junction-to-Lead ^C | $R_{\theta JL}$ | 16 | 24 | $^\circ C/W$ |

Electrical Characteristics (T_J=25°C unless otherwise noted)

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|-----------------------------|---------------------------------------|--|-----|----------|----------|-------|
| STATIC PARAMETERS | | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | I _D =250μA, V _{GS} =0V | 30 | | | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} =24V, V _{GS} =0V T _J =55°C | | 0.003 | 1 5 | μA |
| I _{GSS} | Gate-Body leakage current | V _{DS} =0V, V _{GS} = ±20V | | | ±100 | nA |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =10mA | 1.5 | 2 | 3 | V |
| I _{D(ON)} | On state drain current | V _{GS} =4.5V, V _{DS} =5V | 50 | | | A |
| R _{DS(ON)} | Static Drain-Source On-Resistance | V _{GS} =10V, I _D =11.6A T _J =125°C | | 11 17 | 14 21 | mΩ |
| | | V _{GS} =4.5V, I _D =10A | | 17.4 | 22 | mΩ |
| | | | | | | |
| g _{FS} | Forward Transconductance | V _{DS} =5V, I _D =11.6A | | 19 | | S |
| V _{SD} | Diode Forward Voltage | I _S =1A, V _{GS} =0V | | 0.73 | 1 | V |
| I _S | Maximum Body-Diode Continuous Current | | | | 4.5 | A |
| DYNAMIC PARAMETERS | | | | | | |
| C _{iss} | Input Capacitance | V _{GS} =0V, V _{DS} =15V, f=1MHz | | 955 | 1200 | pF |
| C _{oss} | Output Capacitance | | 145 | | pF | |
| C _{rss} | Reverse Transfer Capacitance | | 112 | | pF | |
| R _g | Gate resistance | V _{GS} =0V, V _{DS} =0V, f=1MHz | | 0.5 | 0.85 | Ω |
| SWITCHING PARAMETERS | | | | | | |
| Q _g (10V) | Total Gate Charge | V _{GS} =10V, V _{DS} =15V, I _D =11.6A | | 17 | 24 | nC |
| Q _g (4.5V) | Total Gate Charge | | 9 | 12 | nC | |
| Q _{gs} | Gate Source Charge | | 3.4 | | nC | |
| Q _{gd} | Gate Drain Charge | | 4.7 | | nC | |
| t _{D(on)} | Turn-On DelayTime | V _{GS} =10V, V _{DS} =15V, R _L =1.30Ω, R _{GEN} =3Ω | | 5 | 6.5 | ns |
| t _r | Turn-On Rise Time | | 6 | 7.5 | ns | |
| t _{D(off)} | Turn-Off DelayTime | | 19 | 25 | ns | |
| t _f | Turn-Off Fall Time | | 4.5 | 6 | ns | |
| t _{rr} | Body Diode Reverse Recovery Time | I _F =11.6A, dI/dt=100A/μs | | 19 | 21 | ns |
| Q _{rr} | Body Diode Reverse Recovery Charge | I _F =11.6A, dI/dt=100A/μs | | 9 | 12 | nC |

A: The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t_θ ≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C: The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.

D: The static characteristics in Figures 1 to 6 are obtained using 80 μs pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

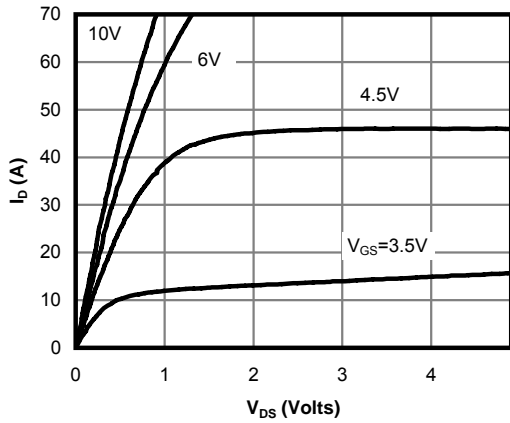


Fig 1: On-Region Characteristics

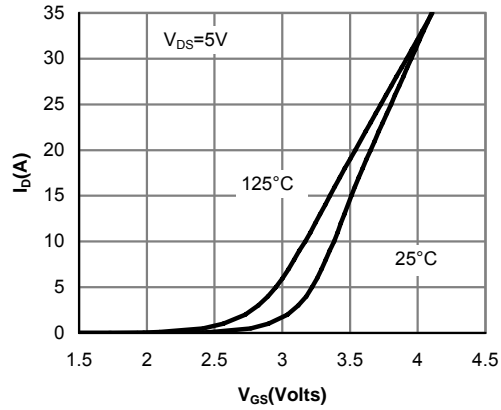


Figure 2: Transfer Characteristics

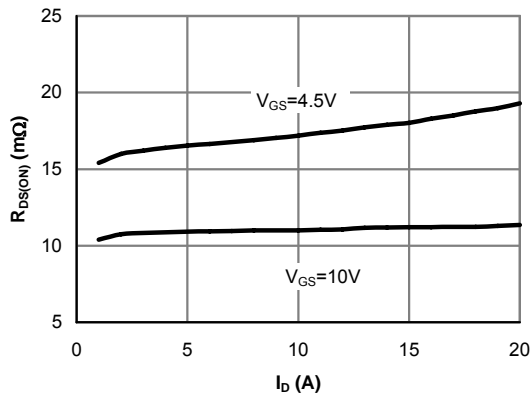


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

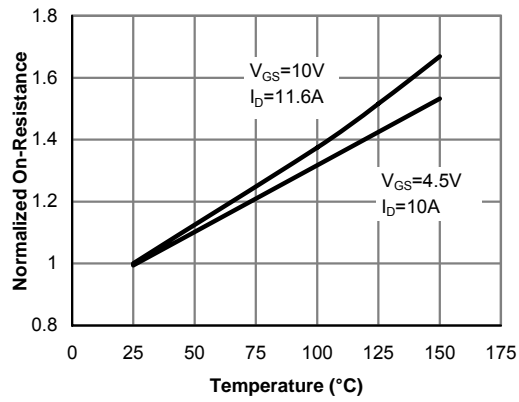


Figure 4: On-Resistance vs. Junction Temperature

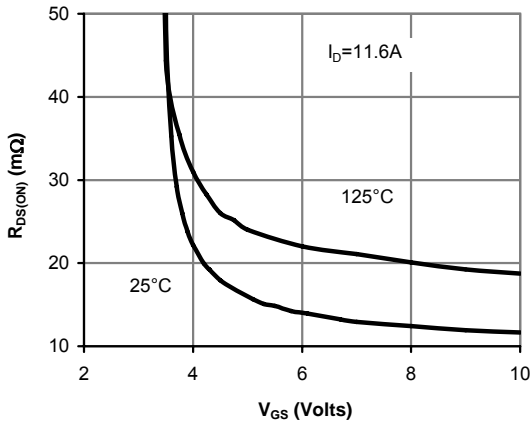


Figure 5: On-Resistance vs. Gate-Source Voltage

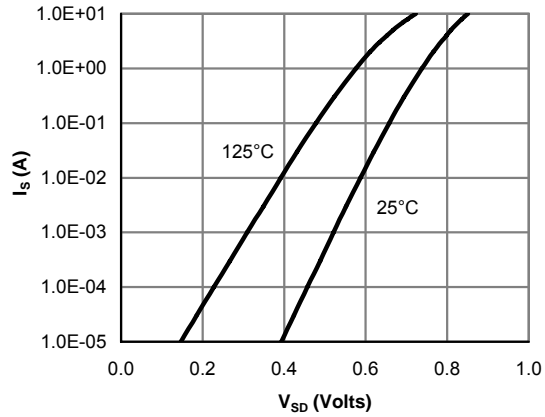


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

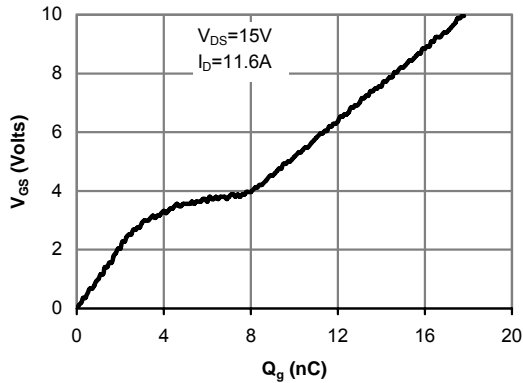


Figure 7: Gate-Charge Characteristics

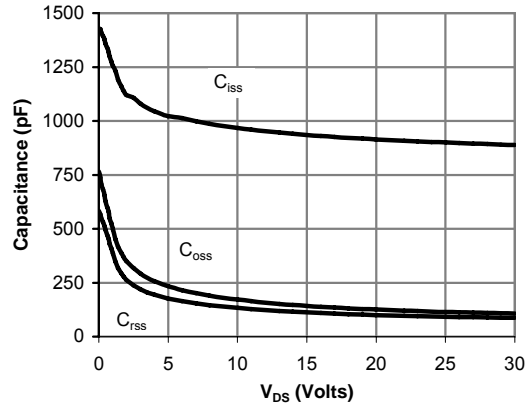


Figure 8: Capacitance Characteristics

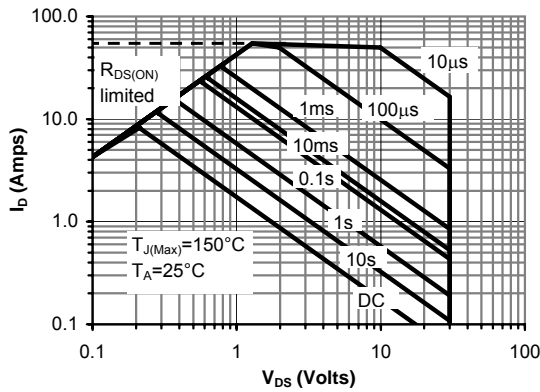


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

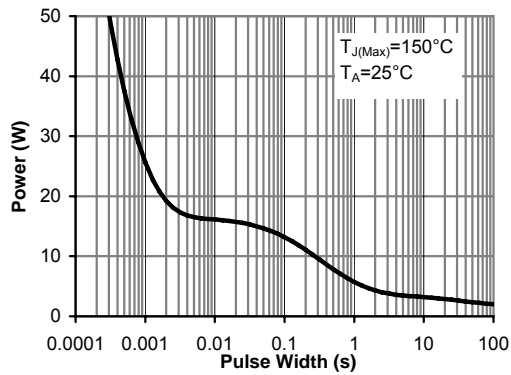


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

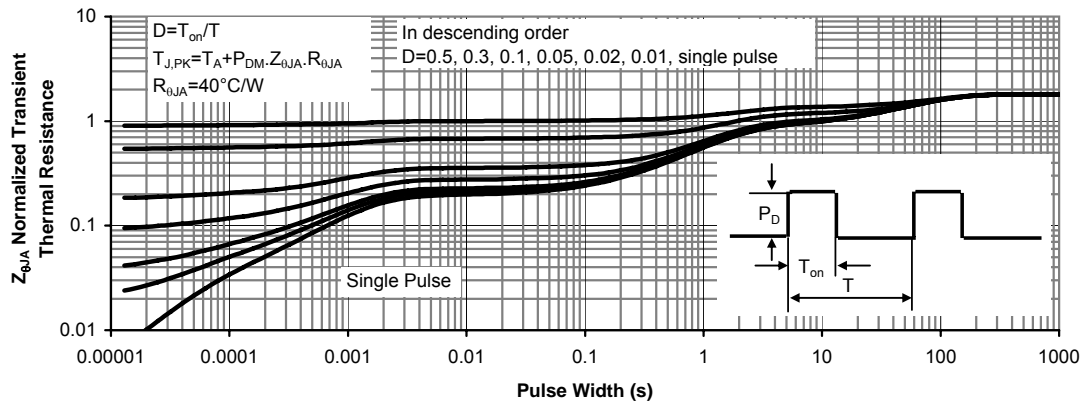


Figure 11: Normalized Maximum Transient Thermal Impedance